

# List of Publications

*Frits Vaandrager*

October 21, 2009

My Hirsch index according to Google Scholar is 32 (October 2009). I have indicated the number of citations to my most cited publications according to Google Scholar. The data from Google Scholar may contain mistakes, and when interpreting these figures a large error margin (100%?) should be used. For some of the conferences I also listed the acceptance ratio.

## Books

1. J. Ouaknine and F.W. Vaandrager (editors). Proceedings of the 7th International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS 2009), Budapest, Hungary, September 14-16, 2009. Proceedings. LNCS 5813, Springer, X, 285 pages. 2009. ISBN 978-3-642-04367-3.
2. D.K. Kaynar, N.A. Lynch, R. Segala and F.W. Vaandrager. The Theory of Timed I/O Automata. Synthesis Lecture on Computer Science, Morgan & Claypool Publishers, 101 pages. 2006. ISBN 159829010X. **(115 citations in Google Scholar)**
3. F.W. Vaandrager and J.H. van Schuppen, editors. Proceedings of the Second International Workshop on Hybrid Systems: Computation and Control (HSCC'99), Berg en Dal, The Netherlands, March 1999. LNCS 1569, Springer-Verlag, X, 271 pages. 1999.
4. G. Rozenberg and F.W. Vaandrager, editors. Lectures on Embedded Systems. LNCS 1494, Springer-Verlag, VIII, 423 pages. 1998.
5. Algebraic Techniques for Concurrency and Their Application. Ph.D. Thesis, University of Amsterdam, February 1990. CWI, Amsterdam, 244 pages. 1989.

## Journal Papers

1. J. Berendsen, D. Jansen J. Schmaltz and F.W. Vaandrager. The Axiomatization of Overriding and Update. Accepted, subject to revision, for *Journal of Applied Logic*, 2009.
2. J. Berendsen, B. Gebremichael, F.W. Vaandrager, and Miaomiao Zhang. Formal Specification and Analysis of Zeroconf using Uppaal. To appear in *ACM Transactions on Embedded Computing Systems*, 2009.
3. R. Hamberg and F.W. Vaandrager. Using Model Checkers in an Introductory Course on Operating Systems. *ACM SIGOPS Operating Systems Review* 42(6):101-111, October 2008.
4. L. Cheung, M.I.A. Stoelinga and F.W. Vaandrager. A Testing Scenario for Probabilistic Processes. In: *Journal of the ACM* 54(6), December 2007. **(For this paper, the second author was awarded the 2008 Professor De Winter prize, 2500 Euro, for the best publication in an international journal by a female scientist at the University of Twente.)**

5. N.A. Lynch, R. Segala and F.W. Vaandrager. Observing Branching Structure Through Probabilistic Contexts. In: *SIAM Journal on Computing* 37(4):977-1013, 2007.
6. F.W. Vaandrager and A.L. de Groot. Analysis of a Biphase Mark Protocol with Uppaal and PVS. In *Formal Aspects of Computing Journal* 18(4):433-458, December 2006. **(27 citations in Google Scholar)**
7. L. Cheung, N.A. Lynch, R. Segala and F.W. Vaandrager. Switched PIOA: Parallel Composition via Distributed Scheduling. In *Theoretical Computer Science* 365(1-2):83-108, 2006.
8. M. Hendriks, N.J.M. van den Nieuwelaar and F.W. Vaandrager. Model Checker Aided Design of a Controller for a Wafer Scanner. In *International Journal on Software Tools for Technology Transfer (STTT)* 8(6):633-647, Special Section on Quantitative Analysis of Real-time Embedded Systems, 2006.
9. W.O.D. Griffioen and F.W. Vaandrager. A Theory of Normed Simulations. In *ACM Transactions on Computational Logic (TOCL)* 5(4):577-610, October 2004.
10. N.A. Lynch, R. Segala and F.W. Vaandrager. Hybrid I/O automata. *Information and Computation* 185(1):105-157, August 2003. **(361 citations in Google Scholar)**
11. T.S. Hune, J.M.T. Romijn, M.I.A. Stoelinga and F.W. Vaandrager. Linear parametric model checking of timed automata. *The Journal of Logic and Algebraic Programming* 52-53:183-220, 2002. **(68 citations in Google Scholar)**
12. J.G. Springintveld, F.W. Vaandrager and P.R. D'Argenio. Testing Timed Automata. *Theoretical Computer Science* 254(1-2):225-257, March 2001. **(148 citations in Google Scholar)**
13. M.C.A. Devillers, W.O.D. Griffioen, J.M.T. Romijn and F.W. Vaandrager. Verification of a Leader Election Protocol: Formal Methods Applied to IEEE 1394. *Formal Methods in System Design*, 16(3):307-320, June 2000. **(79 citations in Google Scholar)**
14. R.J. van Glabbeek and F.W. Vaandrager. The difference between splitting in  $n$  and  $n + 1$ . *Information and Computation*, 136(2):109-142, August 1997.
15. J.M.T. Romijn and F.W. Vaandrager. A note on fairness in I/O automata. *Information Processing Letters*, 59(5):245-250, 1996.
16. N.A. Lynch and F.W. Vaandrager. Action transducers and timed automata. *Formal Aspects of Computing*, 8(5):499-538, Sept-Oct 1996. **(48 citations in Google Scholar)**
17. N.A. Lynch and F.W. Vaandrager. Forward and backward simulations, II: Timing-based systems. *Information and Computation*, 128(1):1-25, July 1996.
18. N.A. Lynch and F.W. Vaandrager. Forward and backward simulations, I: Untimed systems. *Information and Computation*, 121(2):214-233, September 1995. **(275 citations in Google Scholar)**
19. R. De Nicola and F.W. Vaandrager. Three logics for branching bisimulation. *Journal of the ACM*, 42(2):458-487, March 1995. **(237 citations in Google Scholar)**
20. L. Aceto, B. Bloom and F.W. Vaandrager. Turning SOS rules into equations. *LICS'92 Special Issue of Information and Computation*, 111(1):1-52, May 1994. **(130 citations in Google Scholar)**
21. R.J. van Glabbeek and F.W. Vaandrager. Modular specification of process algebras. *Theoretical Computer Science*, 113(2):293-348, 1993.

22. J.F. Groote and F.W. Vaandrager. Structured operational semantics and bisimulation as a congruence. *Information and Computation*, 100(2):202–260, October 1992. **(359 citations in Google Scholar)**
23. J.C.M. Baeten and F.W. Vaandrager. An algebra for process creation. *Acta Informatica*, 29(4):303–334, 1992. **(31 citations in Google Scholar)**
24. F.W. Vaandrager. Determinism  $\rightarrow$  (event structure isomorphism = step sequence equivalence). *Theoretical Computer Science*, 79:275–294, 1991.

## Conference Papers

1. Mathijs Schuts, Feng Zhu, Faranak Heidarian and Frits Vaandrager. Modelling Clock Synchronization in the Chess gMAC WSN Protocol. To appear in *Proceedings Quantitative Formal Methods: Theory and Applications (QFM'09)*, Eindhoven. November 2009.
2. I. AlAttili, F. Houben, G. Igna, S. Michels, F. Zhu and F.W. Vaandrager. Adaptive Scheduling of Data Paths using Uppaal Tiga. To appear in *Proceedings Quantitative Formal Methods: Theory and Applications (QFM'09)*, Eindhoven. November 2009.
3. F. Heidarian, J. Schmaltz and F.W. Vaandrager. Analysis of a Clock Synchronization Protocol for Wireless Sensor Networks. To appear in *Proceedings 16th International Symposium of Formal Methods (FM2009)*, Eindhoven, the Netherlands, November 2-6, 2009. LNCS, Springer-Verlag. **(Acceptance rate 26%)**
4. F.W. Vaandrager, D.N. Jansen and E. Koopmans. Een Module over Model Checking voor het VWO. In *Proceedings NIOC 2009*, Utrecht, 7-8 April 2009. To appear.
5. J. Berendsen and F.W. Vaandrager. Compositional Abstraction in Real-Time Model Checking. In *Proceedings Sixth International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS 2008)*, September 15-17, 2008, Saint-Malo, France. 2008. LNCS 5215, pp. 233-249, Springer-Verlag.
6. G. Igna, V. Kannan, Y. Yang, T. Basten, M. Geilen, F.W. Vaandrager, M. Voorhoeve, S. De Smet, and L. Somers. Formal Modeling and Scheduling of Data Paths of Digital Document Printers. In *Proceedings Sixth International Conference on Formal Modeling and Analysis of Timed Systems (FORMATS 2008)*, September 15-17, 2008, Saint-Malo, France. 2008. LNCS 5215, pp. 169-186, Springer-Verlag.
7. B. Gebremichael, F.W. Vaandrager and M. Zhang. Analysis of a Protocol for Dynamic Configuration of IPv4 Link Local Addresses using Uppaal. In *Proceedings of the 6th Annual ACM & IEEE Conference on Embedded Software (EMSOFT 2006)*, Seoul, South Korea, October 22-25, 2006. ACM Press, pages 242-251. **(Acceptance rate 33%)**
8. B. Gebremichael, F.W. Vaandrager, M. Zhang, K. Goossens, E. Rijkema and A. Rădulescu. Deadlock Prevention in the Aethereal Protocol. In D. Borriore and W. Paul, editors. *Proceedings 13th IFIP Advanced Research Working Conference on Correct Hardware Design and Verification Methods (CHARME'05)*, Saarbrücken, Germany, October 3-6, 2005. LNCS 3725, pages 345–348. Springer-Verlag, 2005.
9. B. Gebremichael and F.W. Vaandrager. Specifying Urgency in Timed I/O Automata. In B.K. Aichernig and B. Beckert, editors. *Proceedings of the 3rd IEEE International Conference on Software Engineering and Formal Methods (SEFM 2005)*, Koblenz, Germany, September 5-9, 2005. IEEE Computer Society, pages 64-73. **(Acceptance rate 33%)**

10. M. Hendriks, N.J.M. van den Nieuwelaar and F.W. Vaandrager. Model Checker Aided Design of a Controller for a Wafer Scanner. In T. Margaria, B. Steffen, A. Philippou and M. Reitenspiess, editors. *Preliminary Proceedings 1st International Symposium on Leveraging Applications of Formal Methods (ISOLA'04)*, Paphos, Cyprus, 30 October - 2 November 2004. Technical Report TR-2004-6, Department of Computer Science, University of Cyprus, pages 201-209. **(This paper is referred to in patent application ASML ref. P-1784.010.)**
11. L. Cheung, N.A. Lynch, R. Segala and F.W. Vaandrager. Switched Probabilistic I/O automata. In Z. Liu and K. Araki, editors. *Proceedings First International Colloquium on Theoretical Aspects of Computing (ICTAC2004)*, Guiyang, China 20-24 September 2004. LNCS 3407, pages 494-510. Springer Verlag, 2005. **(Acceptance rate 31%)**
12. B. Gebremichael and F.W. Vaandrager. Control Synthesis for a Smart Card Personalization System Using Symbolic Model Checking. In K.G. Larsen and P. Niebert, editors. *Proceedings First International Workshop on Formal Modeling and Analysis of Timed Systems (FORMATS 2003)*, September 6-7 2003, Marseille, France. LNCS 2791, pages 189-203. Springer-Verlag, 2004. **(Acceptance rate 53%)**
13. M. Hendriks, G. Behrmann, K.G. Larsen, P. Niebert and Frits Vaandrager. Adding Symmetry Reduction to Uppaal. In K.G. Larsen and P. Niebert, editors. *Proceedings First International Workshop on Formal Modeling and Analysis of Timed Systems (FORMATS 2003)*, September 6-7 2003, Marseille, France. LNCS 2791, pages 46-59. Springer-Verlag, 2004. **(Acceptance rate 53%) (40 citations in Google Scholar)**
14. D.K. Kaynar, N.A. Lynch, R. Segala and F.W. Vaandrager. A Framework for Modelling Timed Systems with Restricted Hybrid Automata. In *Proceedings 24th IEEE International Real-Time Systems Symposium (RTSS03)*, December 3-5, 2003, Cancun, Mexico. IEEE Computer Society, pages 166-177. **(Acceptance rate 26%) (46 citations in Google Scholar)**
15. R.J. van Glabbeek and F.W. Vaandrager. Bundle Event Structures and CCSP. In R. Amadio and D. Lugiez, editors, *Proceedings 14th International Conference on Concurrency Theory (CONCUR 2003)*, Marseille, France, September 3-5, 2003. LNCS 2761, pages 57-71. Springer-Verlag, 2003. **(Acceptance rate 27%)**
16. N.A. Lynch, R. Segala and F.W. Vaandrager. Compositionality for Probabilistic Automata. In R. Amadio and D. Lugiez, editors, *Proceedings 14th International Conference on Concurrency Theory (CONCUR 2003)*, Marseille, France, September 3-5, 2003. LNCS 2761, pages 208-221. Springer-Verlag, 2003. **(Acceptance rate 27%) (32 citations in Google Scholar)**
17. M. Hendriks, N.J.M. van den Nieuwelaar and F.W. Vaandrager. Recognizing Finite Repetitive Scheduling Patterns in Manufacturing Systems. In G. Kendall, E. Burke and S. Petrovic, editors. *Proceedings of the 1st Multidisciplinary International Conference on Scheduling: Theory and Applications (MISTA 2003)*, Nottingham, UK, August 13-16, 2003, Volume I, pages 291-319. The University of Nottingham, UK, 2003. ISBN 0-9545821-0-1.
18. H. Bohnenkamp, P. van der Stok, H. Hermanns, and F. Vaandrager. Cost-Optimisation of the IPv4 Zeroconf Protocol. In *Proceedings of the 2003 International Conferences on Dependable Systems and Networks*, June 22-25, San Fransisco CA, IEEE CS Press, pages 531-540, June 2003. **(Acceptance rate 35%) (28 citations in Google Scholar)**
19. M.I.A. Stoelinga and F.W. Vaandrager. A Testing Scenario for Probabilistic Automata. In J.C.M. Baeten, J.K. Lenstra, J. Parrow and G.J. Woeginger, editors, *Proceedings ICALP2003*, LNCS 2719, pages 407-418. Springer-Verlag, 2003. **(Acceptance rate 40%) (This paper received the EATCS prize for best track B paper of ICALP 2003.) (30 citations in Google Scholar)**

20. A. Fehnker, M. Zhang and F.W. Vaandrager. Modeling and Verifying a Lego Car Using Hybrid I/O Automata. In *Third International Conference on Quality Software (QSIC 2003)*, Dallas, Texas, USA, November 6 - 7, IEEE Computer Society Press, 2003. **(Acceptance rate 30%)**
21. N.A. Lynch, R. Segala and F.W. Vaandrager. Hybrid I/O automata revisited. In M.D. Di Benedetto and A.L. Sangiovanni-Vincentelli, editors. *Proceedings HSCC'01*, Rome, Italy, March 2001. LNCS 2034, pages 403–417. Springer-Verlag, 2001. **(86 citations in Google Scholar) (Acceptance rate 44%)**
22. G. Behrmann, A. Fehnker, T.S. Hune, K.G. Larsen, P. Pettersson, J.M.T. Romijn, and F.W. Vaandrager. Minimum-cost reachability for priced timed automata. In M.D. Di Benedetto and A.L. Sangiovanni-Vincentelli, editors. *Proceedings HSCC'01*, Rome, Italy, March 2001. LNCS 2034, pages 147–161. Springer-Verlag, 2001. **(133 citations in Google Scholar) (Acceptance rate 44%)**
23. T.S. Hune, J.M.T. Romijn, M.I.A. Stoelinga and F.W. Vaandrager. Linear parametric model checking of timed automata. In T. Margaria and W. Yi, editors. *Proceedings TACAS'2001*, Genova, Italy, April 2001. LNCS 2031, pages 189–203. Springer-Verlag, 2001. **(68 citations in Google Scholar) (Acceptance rate 29%)**
24. G. Behrmann, T.S. Hune and F.W. Vaandrager. Distributed Timed Model Checking - How the Search Order Matters. In E.A. Emerson and A.P. Sistla, editors. *Proceedings CAV'2000*, Chicago, July 2000. LNCS 1855, pages 216–231. Springer-Verlag, 2000. **(83 citations in Google Scholar) (Acceptance rate 44%)**
25. M.I.A. Stoelinga and F.W. Vaandrager. Root Contention in IEEE 1394. In J.-P. Katoen, editor, *Proceedings 5th Int. AMAST Workshop on Real-Time and Probabilistic Systems (ARTS'99)*, Bamberg, Germany. LNCS 1601, pages 53–74. Springer-Verlag, 1999. **(70 citations in Google Scholar)**
26. H. Dierks, A. Fehnker, A. Mader and F.W. Vaandrager. Operational and logical semantics for polling real-time systems. In A.P. Ravn and H. Rischel, editors, *Proceedings FTRTFT'98*, Lyngby, Denmark, LNCS 1486, pages 29–40. Springer-Verlag, 1998.
27. W.O.D. Griffioen and F.W. Vaandrager. Normed simulations. In A.J. Hu and M.Y. Vardi, editors, *Proceedings CAV'98*, Vancouver, BC, Canada, LNCS 1427, pages 332-344. Springer-Verlag, 1998. **(Acceptance rate 29%)**
28. J.G. Springintveld and F.W. Vaandrager. Minimizable timed automata. In B. Jonsson and J. Parrow, editors, *Proceedings FTRTFT'96*, Uppsala, Sweden, LNCS 1135, pages 130–147. Springer-Verlag, 1996. **(25 citations in Google Scholar)**
29. N.A. Lynch, R. Segala, F.W. Vaandrager and H.B. Weinberg. Hybrid I/O automata. In R. Alur, T.A. Henzinger, and E.D. Sontag, editors, *Hybrid Systems III*, LNCS 1066, pages 496–510. Springer-Verlag, 1996. **(61 citations in Google Scholar)**
30. F.W. Vaandrager. Verification of a distributed summation algorithm. In I. Lee and S.A. Smolka, editors, *Proceedings 6<sup>th</sup> International Conference on Concurrency Theory (CONCUR'95)*, Philadelphia, PA, USA, August 1995, LNCS 962, pages 190–203. Springer-Verlag, 1995.
31. D.J.B. Bosscher, I. Polak and F.W. Vaandrager. Verification of an audio control protocol. In H. Langmaack, W.P. de Roever, and J. Vytupil, editors, *Proceedings 3<sup>rd</sup> International School and Symposium on Formal Techniques in Real Time and Fault Tolerant Systems (FTRTFT'94)*, Lübeck, Germany, September 1994, LNCS 863, pages 170–192. Springer-Verlag, 1994. **(This paper received the best paper award of FTRTFT'94.) (108 citations in Google Scholar)**

32. L. Helminck, M.P.A. Sellink and F.W. Vaandrager. Proof-checking a data link protocol. In H. Barendregt and T. Nipkow, editors, *Proceedings International Workshop TYPES'93*, Nijmegen, The Netherlands, May 1993. LNCS 806, pages 127–165. Springer-Verlag, 1994. **(103 citations in Google Scholar)**
33. F.W. Vaandrager. Expressiveness results for process algebras. In J.W. de Bakker, W.P. de Roever, and G. Rozenberg, editors, *Proceedings REX Workshop on Semantics: Foundations and Applications*. LNCS 666, pages 609–638. Springer-Verlag, 1993. **(38 citations in Google Scholar)**
34. F.W. Vaandrager and N.A. Lynch. Action transducers and timed automata. In W.R. Cleaveland, editor, *Proceedings 3<sup>rd</sup> International Conference on Concurrency Theory (CONCUR'92)*, Stony Brook, New York. LNCS 630, pages 436–455. Springer-Verlag, 1992. **(33 citations in Google Scholar)**
35. L. Aceto, B. Bloom and F.W. Vaandrager. Turning SOS rules into equations. In *Proceedings 7<sup>th</sup> Annual Symposium on Logic in Computer Science (LICS'92)*, Santa Cruz, California, pages 113–124. IEEE Computer Society Press, 1992.
36. N.A. Lynch and F.W. Vaandrager. Forward and backward simulations for timing-based systems. In J.W. de Bakker, C. Huizing, W.-P de Roever, and G. Rozenberg, editors, *Proceedings REX Workshop on Real-Time: Theory in Practice*. LNCS 600, pages 397–446. Springer-Verlag, 1992. **(283 citations in Google Scholar)**
37. F.W. Vaandrager. On the relationship between process algebra and input/output automata (extended abstract). In *Proceedings 6<sup>th</sup> Annual Symposium on Logic in Computer Science (LICS'91)*, Amsterdam, pages 387–398. IEEE Computer Society Press, 1991. **(83 citations in Google Scholar)**
38. R.J. van Glabbeek and F.W. Vaandrager. The difference between splitting in  $n$  and  $n + 1$ . In E. Best and G. Rozenberg, editors, *Proceedings 3<sup>rd</sup> Workshop on Concurrency and Compositionality*, Goslar, GMD-Studien Nr. 191, pages 117–121. Universität Hildesheim, 1991.
39. R. De Nicola, U. Montanari and F.W. Vaandrager. Back and forth bisimulations. In J.C.M. Baeten and J.W. Klop, editors, *Proceedings First International Conference on Concurrency Theory (CONCUR'90)*, Amsterdam. LNCS 458, pages 152–165. Springer-Verlag, 1990. **(73 citations in Google Scholar)**
40. J.F. Groote and F.W. Vaandrager. An efficient algorithm for branching bisimulation and stuttering equivalence. In M.S. Paterson, editor, *Proceedings 17<sup>th</sup> Int'l Coll. Automata, Languages and Programming (ICALP'90)*, Warwick. LNCS 443, pages 626–638. Springer-Verlag, 1990. **(155 citations in Google Scholar)**
41. R. De Nicola and F.W. Vaandrager. Three logics for branching bisimulation (extended abstract). In *Proceedings 5<sup>th</sup> Annual Symposium on Logic in Computer Science (LICS'90)*, Philadelphia, pages 118–129. IEEE Computer Society Press, 1990.
42. R. De Nicola and F.W. Vaandrager. Action versus state based logics for transition systems. In I. Guessarian, editor, *Semantics of Systems of Concurrent Processes, Proceedings LITP Spring School on Theoretical Computer Science*, La Roche Posay, France. LNCS 469, pages 407–419. Springer-Verlag, 1990. **(197 citations in Google Scholar)**
43. J.F. Groote and F.W. Vaandrager. Structured operational semantics and bisimulation as a congruence (extended abstract). In G. Ausiello, M. Dezani-Ciancaglini, and S. Ronchi Della Rocca, editors, *Proceedings 16<sup>th</sup> Int'l Coll. Automata, Languages and Programming (ICALP'89)*, Stresa. LNCS 372, pages 423–438. Springer-Verlag, 1989.

44. R.J. van Glabbeek and F.W. Vaandrager. Modular specifications in process algebra – with curious queues (extended abstract). In M.Wirsing and J.A. Bergstra, editors, *Proceedings METEOR Workshop on Algebraic Methods: Theory, Tools and Applications*. LNCS 394, pages 465–506. Springer-Verlag, 1989.
45. R.J. van Glabbeek and F.W. Vaandrager. Petri net models for algebraic theories of concurrency. In J.W. de Bakker, A.J. Nijman, and P.C. Treleaven, editors, *Proceedings PARLE conference, Eindhoven, Vol. II (Parallel Languages)*. LNCS 259, pages 224–242. Springer-Verlag, 1987. **(218 citations in Google Scholar)**

## Chapters in Books (No Anonymous Peer Reviewing)

1. F.W. Vaandrager Does it Pay Off? Model-Based Verification and Validation of Embedded Systems! In PROGRESS White Papers 2006. STW, pages 43-66. 2006. ISBN-10: 90-73461-00-6 and ISBN-13: 978-90-73461-00-0.
2. A. Fehnker, M. Zhang and F.W. Vaandrager. Modeling and Verifying a Lego Car Using Hybrid I/O Automata. M. Broy and M. Pizka, editors. *Models, Algebras, and Logic of Engineering Software*, Nato ASI Series III: Computer and Systems Sciences, Volume 191, pages 385–402, IOS Press, 2003.
3. F.W. Vaandrager. Hybrid systems. In *Images of SMC Research 1996*, pages 305–316, Stichting Mathematisch Centrum, Amsterdam, 1996.
4. D.J.B. Bosscher, I. Polak and F.W. Vaandrager. Verification of an audio control protocol. In T. Rus and C. Rattray, editors, *Theories and Experiences for Real-Time System Development*, AMAST Series in Computing, Vol 2, pages 147–176, World Scientific, Singapore, 1994.
5. F.W. Vaandrager. Verification of a distributed summation algorithm. In K.R. Apt, A. Schrijver, and N.M. Temme, editors, *From Universal Morphisms to Megabytes – a Baayen Space Odyssey*, pages 593–608, CWI, Amsterdam, December 1994.
6. F.W. Vaandrager. Informatica en rationalisme. In H.J.M. Goeman and W.A. Kusters, editors, *Liber Amicorum voor A. Ollongren*, Department of Computer Science, Leiden University, December 1993. (In Dutch)
7. F.W. Vaandrager. Two simple protocols. In J.C.M. Baeten, editor, *Applications of process algebra*, Cambridge Tracts in Theoretical Computer Science 17, pages 23–44. Cambridge University Press, 1990.
8. F.W. Vaandrager. Process algebra semantics of POOL. In J.C.M. Baeten, editor, *Applications of process algebra*, Cambridge Tracts in Theoretical Computer Science 17, pages 173–236. Cambridge University Press, 1990. **(43 citations in Google Scholar)**
9. F.W. Vaandrager. Some observations on redundancy in a context. In J.C.M. Baeten, editor, *Applications of process algebra*, Cambridge Tracts in Theoretical Computer Science 17, pages 237–260. Cambridge University Press, 1990.
10. J.C.M. Baeten and F.W. Vaandrager. An algebra for process creation. In *J.W. de Bakker, 25 Jaar Semantiek – Liber Amicorum*, pages 57–81, CWI, Amsterdam, April 1989.

## Other Publications

1. F.W. Vaandrager. Uppaal voor beginners. Course notes, September 2009.
2. F.W. Vaandrager. Wat is model checking? Course notes, March 2008.

3. F.W. Vaandrager. Modelgebaseerde Verificatie en Validatie Loont. *Bits & Chips* 8(15):53–55, 28 September 2006.
4. B.P.F. Jacobs and F.W. Vaandrager. Onderzoeksagenda remt innovatief onderzoek. *Automatisering Gids* 37(28):13, 9 July 2004.
5. H. Bohnenkamp, H. Hermanns, M. Zhang and F. Vaandrager. Cost-Optimisation of the IPv4 Zeroconf Protocol. *Proceedings of the 3rd PROGRESS Workshop on Embedded Systems*, Utrecht, the Netherlands, 2002.
6. F.W. Vaandrager. De Verleidingen van het Hitlijstdenken. *Automatisering Gids* 34(19):23, May 12, 2000.
7. F.W. Vaandrager. Quality of Software — Formal Methods and Tools: Research profile for the Computing Science Institute Nijmegen (CSI). Note CSI-N0001, Computing Science Institute, University of Nijmegen, March 2000.
8. F.W. Vaandrager Nederlandse informatici tellen wel mee. *KUnieuws*, 29(24), March 17, 2000.
9. F.W. Vaandrager. Nederlandse Informatici tellen mee! *U-Blad*, 31(27), March 23, 2000.
10. F.W. Vaandrager. Nieuwe Citatie-Index Voorkomt Dubbel Werk. *Automatisering Gids* 34(8):19, February 25, 2000.
11. F.W. Vaandrager. Introduction. In G. Rozenberg and F.W. Vaandrager, editors. *Lectures on Embedded Systems*. LNCS 1494, pages 1–3. Springer-Verlag, 1998.
12. F.W. Vaandrager. Logica is prachtig hulpmiddel bij oplossen informaticaproblemen. *Automatisering Gids* 32(13):17, March 27, 1998.
13. R. De Nicola, U. Goltz and F.W. Vaandrager (editors). Expressiveness in Concurrency. Dagstuhl-Seminar-Report; 156 16.09.-20.09.96 (9638), IBFI GmbH, Schloss Dagstuhl, Wadern, Germany, 1997.
14. F.W. Vaandrager. De Ingebouwde Informatica. Katholieke Universiteit Nijmegen, December 1996. Inaugural speech, ISBN 90-9010201-9.
15. D.K. Hammer and F.W. Vaandrager. Ingebouwde systemen nog verre van feilloos. *Automatisering Gids* 30(17):17, April 26, 1996. An extended abstract of this paper has appeared as: Ingebouwde Systemen, in *VIE-jaarboek 1996*, pages 54–62, Vereniging van Informaticaingenieurs Eindhoven, 1996.
16. F.A.J. Birrer & F.W. Vaandrager. Politie-informatiesystemen, automatisering en privacy. In *Kwartaalschrift Privacy en Persoonsregistratie*, 10(3):15–19, 1985.

## Technical Reports

1. J. Berendsen, B. Gebremichael, F.W. Vaandrager, and Miaomiao Zhang. Formal Specification and Analysis of Zeroconf using Uppaal. Technical Report ICIS-R07032, ICIS, Radboud University Nijmegen, December 2007.
2. R. Hamberg and F.W. Vaandrager. Using Model Checkers in an Introductory Course on Operating Systems. Technical Report ICIS-R07031, ICIS, Radboud University Nijmegen, December 2007.
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